

The while Macro Instructions of SCM_{FSA} . Part II

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Summary. An attempt to use the `while` macro, [14], was the origin of writing this article. The `while` semantics, as given by J.-C. Chen, is slightly extended by weakening its correctness conditions and this forced a quite straightforward remake of a number of theorems from [14]. Numerous additional properties of the `while` macro are then proven. In the last section, we define a macro instruction computing the `fusc` function (see the SCM program computing the same function in [10]) and prove its correctness.

MML Identifier: `SCMFSA9A`.

The papers [17], [15], [21], [19], [26], [7], [11], [12], [13], [24], [6], [29], [9], [27], [28], [4], [5], [3], [1], [2], [23], [22], [14], [8], [16], [18], [25], and [20] provide the notation and terminology for this paper.

1. ARITHMETIC PRELIMINARIES

We follow the rules: k, m, n are natural numbers, i, j are integers, and r is a real number.

The scheme *MinPred* deals with a unary functor \mathcal{F} yielding a natural number and a unary predicate \mathcal{P} , and states that:

There exists k such that $\mathcal{P}[k]$ and for every n such that $\mathcal{P}[n]$ holds
 $k \leq n$

provided the parameters meet the following condition:

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- For every k holds $\mathcal{F}(k+1) < \mathcal{F}(k)$ or $\mathcal{P}[k]$.

We now state several propositions:

- (1) n is odd iff there exists a natural number k such that $n = 2 \cdot k + 1$.
- (2) If $0 \leq r$, then $0 \leq \lfloor r \rfloor$.
- (3) If $0 < n$, then $0 \leq (m \text{ qua integer}) \div n$.
- (4) If $0 < i$ and $1 < j$, then $i \div j < i$.
- (5) If $0 < n$, then $(m \text{ qua integer}) \div n = m \div n$ and $(m \text{ qua integer}) \bmod n = m \bmod n$.

2. $\mathbf{SCM}_{\text{FSA}}$ PRELIMINARIES

In the sequel l is an instruction-location of $\mathbf{SCM}_{\text{FSA}}$ and i is an instruction of $\mathbf{SCM}_{\text{FSA}}$.

Next we state several propositions:

- (6) Let N be a non empty set with non empty elements, S be a halting von Neumann definite AMI over N , s be a state of S , and k be a natural number. If $\text{CurInstr}((\text{Computation}(s))(k)) = \mathbf{halt}_S$, then $(\text{Computation}(s))(\text{LifeSpan}(s)) = (\text{Computation}(s))(k)$.
- (7) $\text{UsedIntLoc}(l \mapsto i) = \text{UsedIntLoc}(i)$.
- (8) $\text{UsedInt}^* \text{Loc}(l \mapsto i) = \text{UsedInt}^* \text{Loc}(i)$.
- (9) $\text{UsedIntLoc}(\text{Stop}_{\mathbf{SCM}_{\text{FSA}}}) = \emptyset$.
- (10) $\text{UsedInt}^* \text{Loc}(\text{Stop}_{\mathbf{SCM}_{\text{FSA}}}) = \emptyset$.
- (11) $\text{UsedIntLoc}(\text{Goto}(l)) = \emptyset$.
- (12) $\text{UsedInt}^* \text{Loc}(\text{Goto}(l)) = \emptyset$.

For simplicity, we use the following convention: s, s_1, s_2 are states of $\mathbf{SCM}_{\text{FSA}}$, a is a read-write integer location, b is an integer location, f is a finite sequence location, I, J are macro instructions, I_1 is a good macro instruction, and i, j, k are natural numbers.

The following four propositions are true:

- (13) $\text{UsedIntLoc}(\mathbf{if } b = 0 \text{ then } I \text{ else } J) = \{b\} \cup \text{UsedIntLoc}(I) \cup \text{UsedIntLoc}(J)$.
- (14) For every integer location a holds $\text{UsedInt}^* \text{Loc}(\mathbf{if } a = 0 \text{ then } I \text{ else } J) = \text{UsedInt}^* \text{Loc}(I) \cup \text{UsedInt}^* \text{Loc}(J)$.
- (15) $\text{UsedIntLoc}(\mathbf{if } b > 0 \text{ then } I \text{ else } J) = \{b\} \cup \text{UsedIntLoc}(I) \cup \text{UsedIntLoc}(J)$.
- (16) $\text{UsedInt}^* \text{Loc}(\mathbf{if } b > 0 \text{ then } I \text{ else } J) = \text{UsedInt}^* \text{Loc}(I) \cup \text{UsedInt}^* \text{Loc}(J)$.

3. THE **while=0** MACRO INSTRUCTION

Next we state two propositions:

- (17) $\text{UsedIntLoc}(\mathbf{while } b = 0 \mathbf{ do } I) = \{b\} \cup \text{UsedIntLoc}(I)$.
 (18) $\text{UsedInt}^* \text{Loc}(\mathbf{while } b = 0 \mathbf{ do } I) = \text{UsedInt}^* \text{Loc}(I)$.

Let s be a state of $\mathbf{SCM}_{\text{FSA}}$, let a be a read-write integer location, and let I be a macro instruction. The predicate $\text{ProperBodyWhile=0}(a, I, s)$ is defined as follows:

- (Def. 1) For every natural number k such that $(\text{StepWhile=0}(a, I, s))(k)(a) = 0$ holds I is closed on $(\text{StepWhile=0}(a, I, s))(k)$ and halting on $(\text{StepWhile=0}(a, I, s))(k)$.

The predicate $\text{WithVariantWhile=0}(a, I, s)$ is defined by the condition (Def. 2).

- (Def. 2) There exists a function f from \prod (the object kind of $\mathbf{SCM}_{\text{FSA}}$) into \mathbb{N} such that for every natural number k holds $f((\text{StepWhile=0}(a, I, s))(k+1)) < f((\text{StepWhile=0}(a, I, s))(k))$ or $(\text{StepWhile=0}(a, I, s))(k)(a) \neq 0$.

We now state several propositions:

- (19) For every parahalting macro instruction I holds $\text{ProperBodyWhile=0}(a, I, s)$.
 (20) If $\text{ProperBodyWhile=0}(a, I, s)$ and $\text{WithVariantWhile=0}(a, I, s)$, then $\mathbf{while } a = 0 \mathbf{ do } I$ is halting on s and $\mathbf{while } a = 0 \mathbf{ do } I$ is closed on s .
 (21) For every parahalting macro instruction I such that $\text{WithVariantWhile=0}(a, I, s)$ holds $\mathbf{while } a = 0 \mathbf{ do } I$ is halting on s and $\mathbf{while } a = 0 \mathbf{ do } I$ is closed on s .
 (22) If $(\mathbf{while } a = 0 \mathbf{ do } I) + \cdot S_1 \subseteq s$ and $s(a) \neq 0$, then $\text{LifeSpan}(s) = 4$ and for every natural number k holds $(\text{Computation}(s))(k) \upharpoonright D = s \upharpoonright D$, where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
 (23) If I is closed on s and halting on s and $s(a) = 0$, then $(\text{Computation}(s + \cdot (\mathbf{while } a = 0 \mathbf{ do } I) + \cdot S_1))(\text{LifeSpan}(s + \cdot (I + \cdot S_1)) + 3) \upharpoonright D = (\text{Computation}(s + \cdot (I + \cdot S_1)))(\text{LifeSpan}(s + \cdot (I + \cdot S_1))) \upharpoonright D$, where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
 (24) If $(\text{StepWhile=0}(a, I, s))(k)(a) \neq 0$, then $(\text{StepWhile=0}(a, I, s))(k+1) \upharpoonright D = (\text{StepWhile=0}(a, I, s))(k) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
 (25) Suppose I is halting on $\text{Initialize}((\text{StepWhile=0}(a, I, s))(k))$, closed on $\text{Initialize}((\text{StepWhile=0}(a, I, s))(k))$, and parahalting and $(\text{StepWhile=0}(a, I, s))(k)(a) = 0$ and $(\text{StepWhile=0}(a, I, s))(k)(\text{intloc}(0)) = 1$. Then $(\text{StepWhile=0}(a, I, s))(k+1) \upharpoonright D = \text{IExec}(I, (\text{StepWhile=0}(a, I, s))(k)) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

- (26) If $\text{ProperBodyWhile}=0(a, I_1, s)$ or I_1 is parahalting and if $s(\text{intloc}(0)) = 1$, then for every k holds $(\text{StepWhile}=0(a, I_1, s))(k)(\text{intloc}(0)) = 1$.
- (27) If $\text{ProperBodyWhile}=0(a, I, s_1)$ and $s_1 \upharpoonright D = s_2 \upharpoonright D$, then for every k holds $(\text{StepWhile}=0(a, I, s_1))(k) \upharpoonright D = (\text{StepWhile}=0(a, I, s_2))(k) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

Let s be a state of $\mathbf{SCM}_{\text{FSA}}$, let a be a read-write integer location, and let I be a macro instruction. Let us assume that $\text{ProperBodyWhile}=0(a, I, s)$ or I is parahalting and $\text{WithVariantWhile}=0(a, I, s)$. The functor $\text{ExitsAtWhile}=0(a, I, s)$ yielding a natural number is defined by the condition (Def. 3).

(Def. 3) There exists a natural number k such that

- (i) $\text{ExitsAtWhile}=0(a, I, s) = k$,
- (ii) $(\text{StepWhile}=0(a, I, s))(k)(a) \neq 0$,
- (iii) for every natural number i such that $(\text{StepWhile}=0(a, I, s))(i)(a) \neq 0$ holds $k \leq i$, and
- (iv) $(\text{Computation}(s + \cdot ((\mathbf{while} \ a = 0 \ \mathbf{do} \ I) + \cdot S_1)))(\text{LifeSpan}(s + \cdot ((\mathbf{while} \ a = 0 \ \mathbf{do} \ I) + \cdot S_1))) \upharpoonright D = (\text{StepWhile}=0(a, I, s))(k) \upharpoonright D$, where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

One can prove the following two propositions:

- (28) If $s(\text{intloc}(0)) = 1$ and $s(a) \neq 0$, then $\text{IExec}(\mathbf{while} \ a = 0 \ \mathbf{do} \ I, s) \upharpoonright D = s \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (29) If $\text{ProperBodyWhile}=0(a, I, \text{Initialize}(s))$ or I is parahalting and if $\text{WithVariantWhile}=0(a, I, \text{Initialize}(s))$, then $\text{IExec}(\mathbf{while} \ a = 0 \ \mathbf{do} \ I, s) \upharpoonright D = (\text{StepWhile}=0(a, I, \text{Initialize}(s)))(\text{ExitsAtWhile}=0(a, I, \text{Initialize}(s))) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

4. THE $\mathbf{while}>0$ MACRO INSTRUCTION

The following propositions are true:

- (30) $\text{UsedIntLoc}(\mathbf{while} \ b > 0 \ \mathbf{do} \ I) = \{b\} \cup \text{UsedIntLoc}(I)$.
- (31) $\text{UsedInt}^* \text{Loc}(\mathbf{while} \ b > 0 \ \mathbf{do} \ I) = \text{UsedInt}^* \text{Loc}(I)$.

Let s be a state of $\mathbf{SCM}_{\text{FSA}}$, let a be a read-write integer location, and let I be a macro instruction. The predicate $\text{ProperBodyWhile}>0(a, I, s)$ is defined as follows:

- (Def. 4) For every natural number k such that $(\text{StepWhile}>0(a, I, s))(k)(a) > 0$ holds I is closed on $(\text{StepWhile}>0(a, I, s))(k)$ and halting on $(\text{StepWhile}>0(a, I, s))(k)$.

The predicate $\text{WithVariantWhile}>0(a, I, s)$ is defined by the condition (Def. 5).

(Def. 5) There exists a function f from \mathbb{I} (the object kind of $\mathbf{SCM}_{\text{FSA}}$) into \mathbb{N} such that for every natural number k holds $f((\text{StepWhile}>0(a, I, s))(k+1)) < f((\text{StepWhile}>0(a, I, s))(k))$ or $(\text{StepWhile}>0(a, I, s))(k)(a) \leq 0$.

Next we state several propositions:

- (32) For every parahalting macro instruction I holds $\text{ProperBodyWhile}>0(a, I, s)$.
- (33) If $\text{ProperBodyWhile}>0(a, I, s)$ and $\text{WithVariantWhile}>0(a, I, s)$, then **while** $a > 0$ **do** I is halting on s and **while** $a > 0$ **do** I is closed on s .
- (34) For every parahalting macro instruction I such that $\text{WithVariantWhile}>0(a, I, s)$ holds **while** $a > 0$ **do** I is halting on s and **while** $a > 0$ **do** I is closed on s .
- (35) If $(\text{while } a > 0 \text{ do } I) + \cdot S_1 \subseteq s$ and $s(a) \leq 0$, then $\text{LifeSpan}(s) = 4$ and for every natural number k holds $(\text{Computation}(s))(k) \upharpoonright D = s \upharpoonright D$, where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (36) If I is closed on s and halting on s and $s(a) > 0$, then $(\text{Computation}(s + \cdot (\text{while } a > 0 \text{ do } I) + \cdot S_1))(\text{LifeSpan}(s + \cdot (I + \cdot S_1)) + 3) \upharpoonright D = (\text{Computation}(s + \cdot (I + \cdot S_1)))(\text{LifeSpan}(s + \cdot (I + \cdot S_1))) \upharpoonright D$, where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (37) If $(\text{StepWhile}>0(a, I, s))(k)(a) \leq 0$, then $(\text{StepWhile}>0(a, I, s))(k+1) \upharpoonright D = (\text{StepWhile}>0(a, I, s))(k) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (38) Suppose I is halting on $\text{Initialize}((\text{StepWhile}>0(a, I, s))(k))$, closed on $\text{Initialize}((\text{StepWhile}>0(a, I, s))(k))$, and parahalting and $(\text{StepWhile}>0(a, I, s))(k)(a) > 0$ and $(\text{StepWhile}>0(a, I, s))(k)(\text{intloc}(0)) = 1$. Then $(\text{StepWhile}>0(a, I, s))(k+1) \upharpoonright D = \text{IExec}(I, (\text{StepWhile}>0(a, I, s))(k)) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (39) If $\text{ProperBodyWhile}>0(a, I_1, s)$ or I_1 is parahalting and if $s(\text{intloc}(0)) = 1$, then for every k holds $(\text{StepWhile}>0(a, I_1, s))(k)(\text{intloc}(0)) = 1$.
- (40) If $\text{ProperBodyWhile}>0(a, I, s_1)$ and $s_1 \upharpoonright D = s_2 \upharpoonright D$, then for every k holds $(\text{StepWhile}>0(a, I, s_1))(k) \upharpoonright D = (\text{StepWhile}>0(a, I, s_2))(k) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

Let s be a state of $\mathbf{SCM}_{\text{FSA}}$, let a be a read-write integer location, and let I be a macro instruction. Let us assume that $\text{ProperBodyWhile}>0(a, I, s)$ or I is parahalting and $\text{WithVariantWhile}>0(a, I, s)$.

The functor $\text{ExitsAtWhile}>0(a, I, s)$ yields a natural number and is defined by the condition (Def. 6).

- (Def. 6) There exists a natural number k such that
- (i) $\text{ExitsAtWhile}>0(a, I, s) = k$,
 - (ii) $(\text{StepWhile}>0(a, I, s))(k)(a) \leq 0$,

- (iii) for every natural number i such that $(StepWhile>0(a, I, s))(i)(a) \leq 0$ holds $k \leq i$, and
- (iv) $(Computation(s+\cdot((\mathbf{while} \ a > 0 \ \mathbf{do} \ I)+\cdot S_1)))(LifeSpan(s+\cdot((\mathbf{while} \ a > 0 \ \mathbf{do} \ I)+\cdot S_1))) \upharpoonright D = (StepWhile>0(a, I, s))(k) \upharpoonright D$,
where $S_1 = \text{Start-At}(\text{insloc}(0))$ and $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

Next we state several propositions:

- (41) If $s(\text{intloc}(0)) = 1$ and $s(a) \leq 0$, then $\text{IExec}(\mathbf{while} \ a > 0 \ \mathbf{do} \ I, s) \upharpoonright D = s \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (42) If $\text{ProperBodyWhile}>0(a, I, \text{Initialize}(s))$ or I is parahalting and if $\text{WithVariantWhile}>0(a, I, \text{Initialize}(s))$, then $\text{IExec}(\mathbf{while} \ a > 0 \ \mathbf{do} \ I, s) \upharpoonright D = (StepWhile>0(a, I, \text{Initialize}(s)))(ExitsAtWhile>0(a, I, \text{Initialize}(s))) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (43) If $(StepWhile>0(a, I, s))(k)(a) \leq 0$, then for every natural number n such that $k \leq n$ holds $(StepWhile>0(a, I, s))(n) \upharpoonright D = (StepWhile>0(a, I, s))(k) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (44) If $s_1 \upharpoonright D = s_2 \upharpoonright D$ and $\text{ProperBodyWhile}>0(a, I, s_1)$, then $\text{ProperBodyWhile}>0(a, I, s_2)$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.
- (45) Suppose $s(\text{intloc}(0)) = 1$ and $\text{ProperBodyWhile}>0(a, I_1, s)$ and $\text{WithVariantWhile}>0(a, I_1, s)$. Let given i, j . Suppose $i \neq j$ and $i \leq \text{ExitsAtWhile}>0(a, I_1, s)$ and $j \leq \text{ExitsAtWhile}>0(a, I_1, s)$. Then $(StepWhile>0(a, I_1, s))(i) \neq (StepWhile>0(a, I_1, s))(j)$ and $(StepWhile>0(a, I_1, s))(i) \upharpoonright D \neq (StepWhile>0(a, I_1, s))(j) \upharpoonright D$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

Let f be a function from \prod (the object kind of $\mathbf{SCM}_{\text{FSA}}$) into \mathbb{N} . We say that f is on data only if and only if:

- (Def. 7) For all s_1, s_2 such that $s_1 \upharpoonright D = s_2 \upharpoonright D$ holds $f(s_1) = f(s_2)$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

We now state two propositions:

- (46) Suppose $s(\text{intloc}(0)) = 1$ and $\text{ProperBodyWhile}>0(a, I_1, s)$ and $\text{WithVariantWhile}>0(a, I_1, s)$. Then there exists a function f from \prod (the object kind of $\mathbf{SCM}_{\text{FSA}}$) into \mathbb{N} such that f is on data only and for every natural number k holds $f((StepWhile>0(a, I_1, s))(k+1)) < f((StepWhile>0(a, I_1, s))(k))$ or $(StepWhile>0(a, I_1, s))(k)(a) \leq 0$.
- (47) If $s_1(\text{intloc}(0)) = 1$ and $s_1 \upharpoonright D = s_2 \upharpoonright D$ and $\text{ProperBodyWhile}>0(a, I_1, s_1)$ and $\text{WithVariantWhile}>0(a, I_1, s_1)$, then $\text{WithVariantWhile}>0(a, I_1, s_2)$, where $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$.

5. A MACRO FOR THE **fusc** FUNCTION

Let N, r_1 be integer locations. The functor $\text{Fusc_macro}(N, r_1)$ yields a macro instruction and is defined as follows:

(Def. 8) $\text{Fusc_macro}(N, r_1) =$
 SubFrom(r_1, r_1);
 ($n_1 := \text{intloc}(0)$);
 ($a_1 := N$);
 (**while** $a_1 > 0$ **do**
 ($r_2 := 2$);
 Divide(a_1, r_2);
 (**if** $r_2 = 0$ **then**
 Macro(AddTo(n_1, r_1)) **else**
 Macro(AddTo(r_1, n_1))))),
 where $n_1 = 1^{\text{st}}\text{-RWNotIn}(\{N, r_1\})$, $a_1 = 2^{\text{nd}}\text{-RWNotIn}(\{N, r_1\})$, and $r_2 = 3^{\text{rd}}\text{-RWNotIn}(\{N, r_1\})$.

One can prove the following proposition

(48) Let N, r_1 be read-write integer locations. Suppose $N \neq r_1$. Let n be a natural number. If $n = s(N)$, then $(\text{IExec}(\text{Fusc_macro}(N, r_1), s))(r_1) = \text{Fusc}(n)$ and $(\text{IExec}(\text{Fusc_macro}(N, r_1), s))(N) = n$.

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